

<b>FORM PTO-1449 (Modified)</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	<b>ATTY. DOCKET NO.</b> RAL9-99-0056	<b>SERIAL NO.</b> Unknown
	<b>APPLICANT:</b> White et al.	
	<b>FILING DATE:</b> Herewith	<b>GROUP:</b> Unknown

**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
<i>702</i>	5 4 5 7 6 9 7	Oct 10, 1995	Malleo-Roach et al.			
<i>702</i>	5 5 7 2 4 3 7	Nov 5, 1996	Rostoker et al.			

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

**OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)**

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<b>EXAMINER</b>  <i>702</i>	<b>DATE CONSIDERED</b>  2 JULY 2002
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<b>FORM PTO-1449 (Modified)</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	<b>ATTY. DOCKET NO.</b> RAL9-99-0056	<b>SERIAL NO.</b> Unknown
<b>APPLICANT:</b> White et al.		
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**REFERENCE DESIGNATION                      U.S. PATENT DOCUMENTS**

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
JA	5 6 3 3 8 1 2	May 27, 1997	Allen et al.			
JL	4 9 1 4 6 1 2	Apr 3, 1990	Beece et al.			
JA	5 5 1 7 6 3 7	May 14, 1996	Bruce, Jr. et al.			
JA	4 8 1 7 0 9 3	Mar 28, 1989	Jacobs et al.			
JA	5 6 7 5 5 4 5	Oct 7, 1997	Madhavan et al.			
JA	5 1 8 7 7 1 2	Feb 16, 1993	Malleo-Roach et al.			

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

**OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)**

JA  JA		"Versatile Testcase Driver For System Simulation," <i>IBM Technical Disclosure Bulletin</i> , Vol. 32, No. 8A, January 1990.
		"Electronic Card Fault Simulator to Building Block Tester Data Path," <i>IBM Technical Disclosure Bulletin</i> , Vol. 37, No. 08, August 1994.

<b>EXAMINER</b>  <div style="font-size: 2em; font-family: cursive;">JL</div>	<b>DATE CONSIDERED</b>  <div style="font-size: 1.2em; font-family: cursive;">2 JULY 2002</div>
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